

भारतसरकार, रक्षामंत्रालय
रक्षाअनुसंधानतथाविकाससंगठन
डीआरडीओयुवावैज्ञानिकप्रयोगशाला
क्वांटमप्रौद्योगिकी,
हॉलनंबर 1, ग्राउंडफ्लोर, विज्ञान
उपकेंद्र, डीआईएटीपरिसर,
गिरिनगर, पुणे - 411025,
ई-मेल : director.dysl-qt@gov.in



Government of India,
Ministry of Defence
Defence Research and
Development Organisation
**DRDO Young Scientists Lab-
Quantum Technologies**
(Dysl-QT)
Hall No. 1, Ground Floor, Vigyan
Upakendra, DIAT Campus, Girinagar,
Pune – 411025
e-mail: director.dysl-qt@gov.in

Date: 12 May 2023

Advertisement for the selection of Junior Research Fellowship(JRF)

1. DRDO Young Scientist Laboratory- Quantum Technologies (Dysl-QT) is constituted of young scientists to do extensive research and development in the field of Quantum Technologies viz. Quantum Computing, Quantum Communication, Quantum Sensing etc. The lab has association of eminent institutes like, IIT, Central & State Universities and other leading academic institutions. Lab invites application from candidates having strong desire to pursue research in the area of innovative technology and possessing qualifications as under, for the position of Junior Research Fellowship (JRF). Initially, the fellowships shall be for a period of two years (which can be extendable for further two years as per rules in force). The Competent Authority shall operate a panel of selected candidates for filling up the positions from both existing and likely to arise in the future as well, in the order of merit/discipline for a period of one year from the date of declaration of panel list.

S.No.	Title of Fellowship	Minimum eligibility Criteria	Desirable Qualification	No of vacancies
1	JRF	B.E/B.Tech Degree in Electronics & Communication Engg / Electronics/Electrical & Electronics/Electronics & Telecommunication in 1 st Division with Valid NET/GATE qualification (OR) ME/M.Tech Degree in Electronics & Communication / Electronics/Electrical & Electronics/Electronics & Telecommunication Engg in 1 st Division both at Graduate and Postgraduate level.	1) Verilog coding, experience of working with FPGA specially ZYNQ architecture. 2) Hands-on-experience working on FPGAs, bare-metal programming, Linux level programming. 3. Experience in Verilog/ VHDL based programming, high speed communication interfaces like gigabit transceivers, Ethernet etc., error correction protocols and its implementation on FPGA. 4. C, Python based programming, MATLAB. 5. Proficiency in Communication and DSP algorithms. 6. Having knowledge of working in embedded system and FPGA based design. 7. Exposure to Quantum Technologies	03

2. **Upper age limit** :28 years as on the last date of receipt of application. The upper age limit shall be relax able to candidates from SC, ST and OBC (NCL)category as per Govt. rules in vogue.

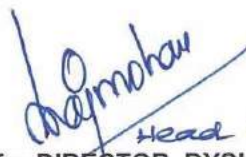
3. **Emolument**: Stipend of Rs. 31000/- per month plus HRA as applicable.

4. The detailed advertisement with application form and other specimen formats are available in the websites www.drdo.gov.in (>What's New> Application for the post of JRF at DYSL-QT, Pune). Interested candidates can also obtain the application form and other details through email: director@dysl-qt.drdo.in The duly filled-in application and self-attested copies of educational qualifications and experience should reach:

The Director,
DRDO Young Scientist Laboratory- Quantum Technology
Defence Research and Development Organisation (DRDO)
Hall No. 1, Ground Floor, Vigyan Upakendra,
DIAT Campus, Girinagar, Pune- 411025, Maharashtra

within 30 days from the date of publication of the advertisement in Employment News. The envelope containing application should be super scribed "Application for JRF" at the top left corner. In addition, they may send a soft copy of the application to director@dyslqt.drdo.in.

5. Mode of selection shall be by Interview. The Date, time, venue and mode of interview will be intimated to the shortlisted candidate by post and email.
6. Candidates working in Govt./Public Sector Undertaking/Autonomous Bodies should apply through proper channel. They are required to submit a signed declaration (Annexure 'II') to the extent that they have informed their Cadre Controlling Authority (CCA) about their application for the fellowship in DRDO and required to produce a " No Objection Certificate" from their CCA as per Annexure 'III' at the time of interview.
7. The Offer of fellowship is purely of temporary in nature for a limited period as per rules in vogue and does not confer any right for absorption in DRDO.
8. Those who were already awarded JRF position in any of the DRDO Laboratories/centers earlier, are not eligible to apply again.
9. Candidates need to attach the proof of CGPA to percentage conversion formula from the University/ Institution along with the application form.


Head Admin.
(for DIRECTOR, DYSL-QT,Pune)